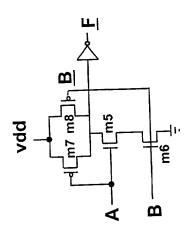
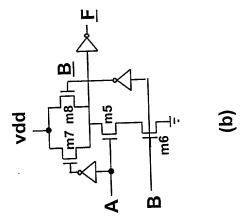
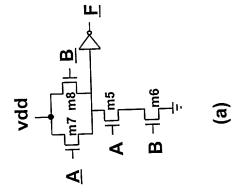


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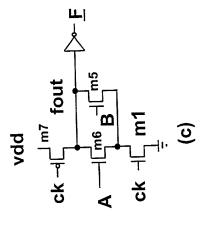


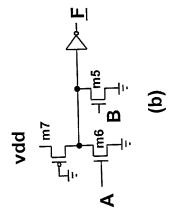




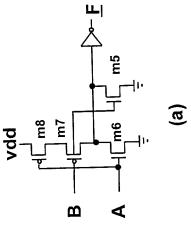
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<u>(၁</u>

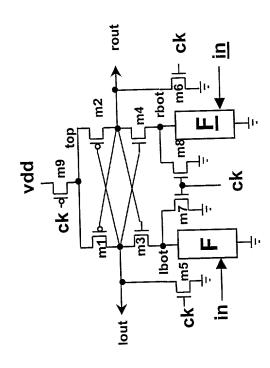




Prior Art



m10 m9 m5a m6a (၁) m4 무 × 4 × m10 m9 8 E 1 [Prior Art m4 шŞ m3 × 4 ٩ B m10 _m_ vdd m3 m4 (a) 4



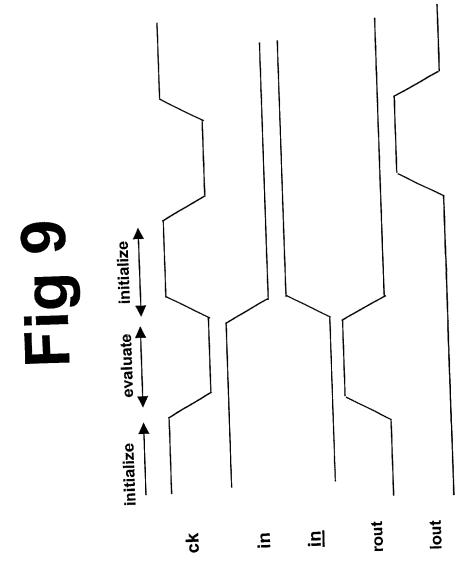


Fig 10A

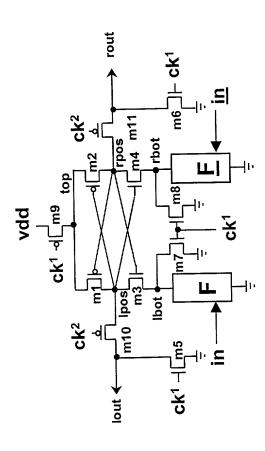


Fig 10B

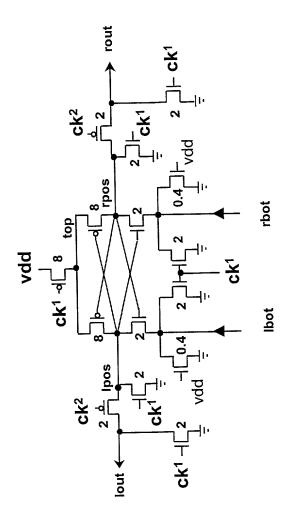


Fig 10C

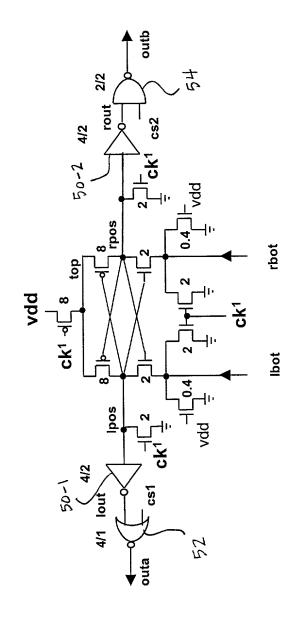
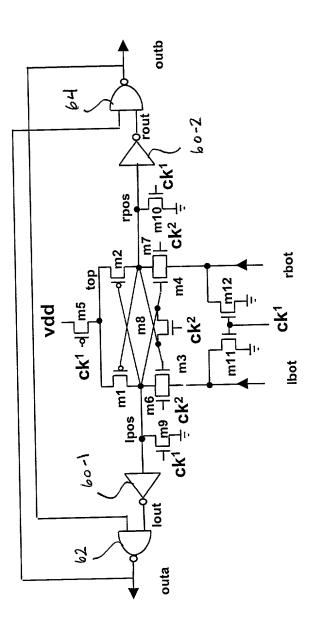
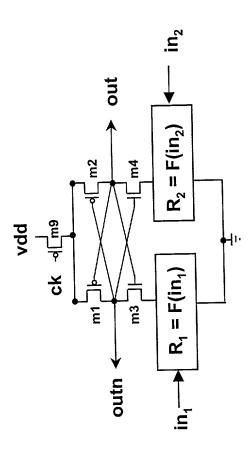
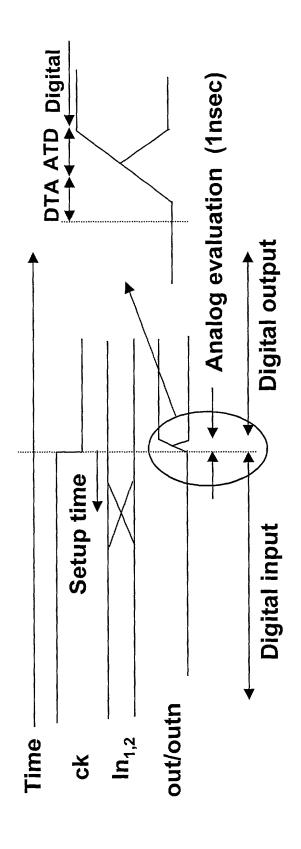
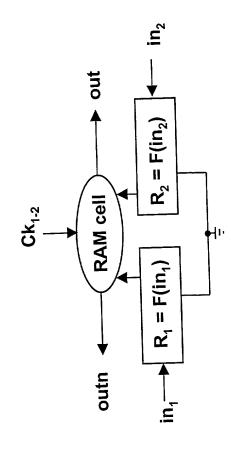


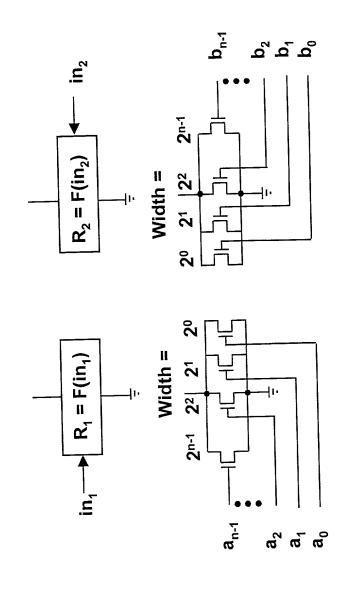
Fig 10D

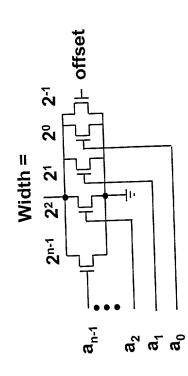


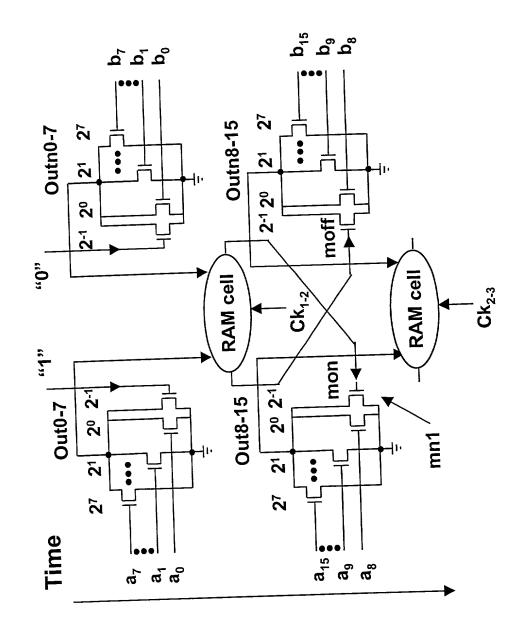


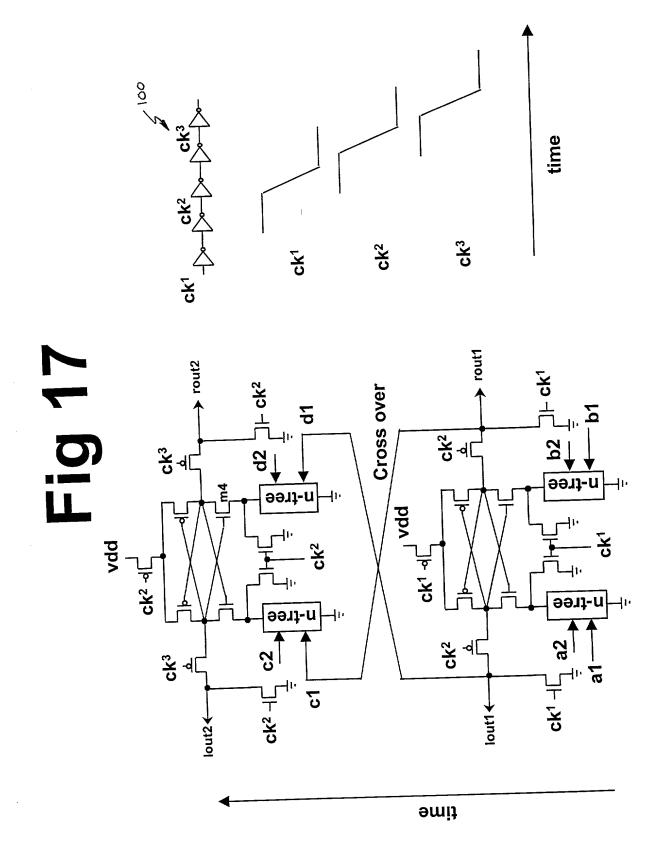


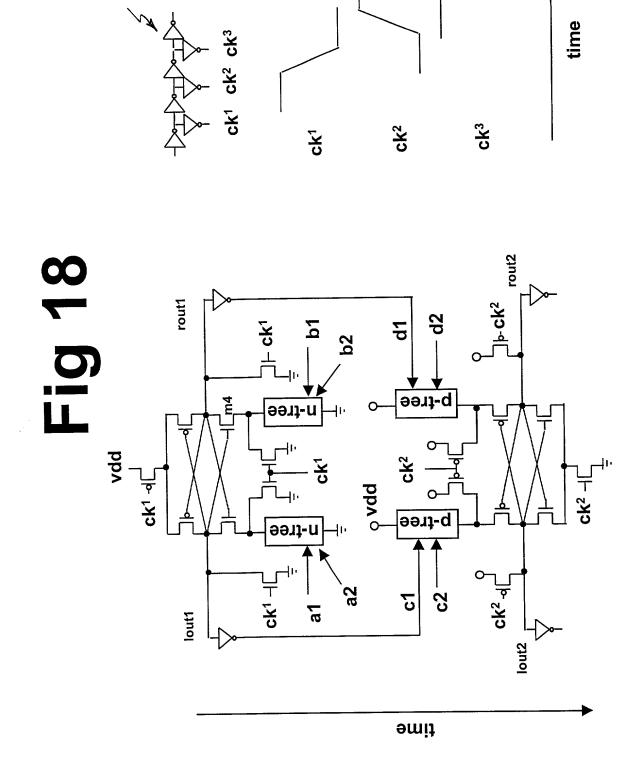


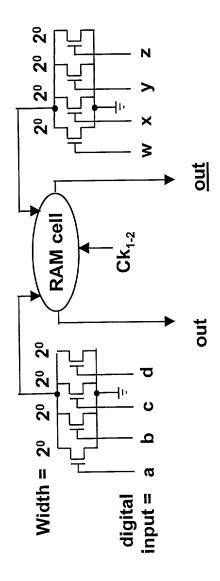


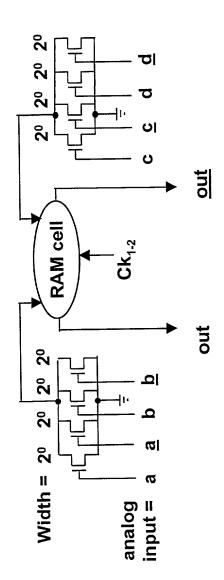


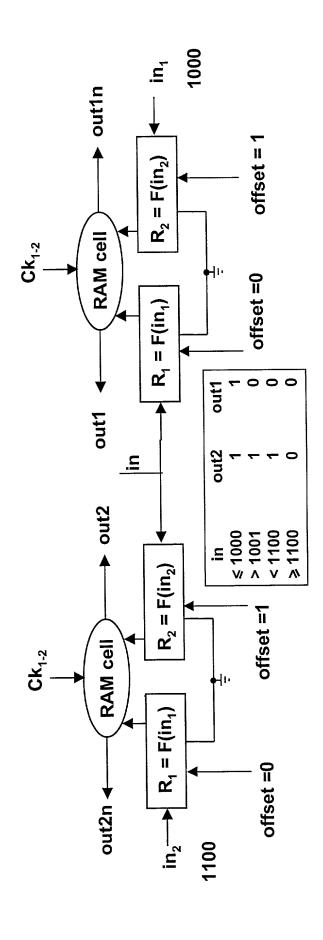


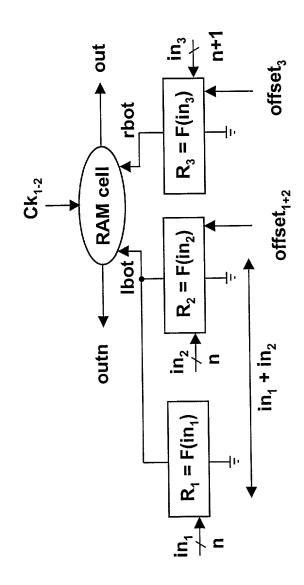












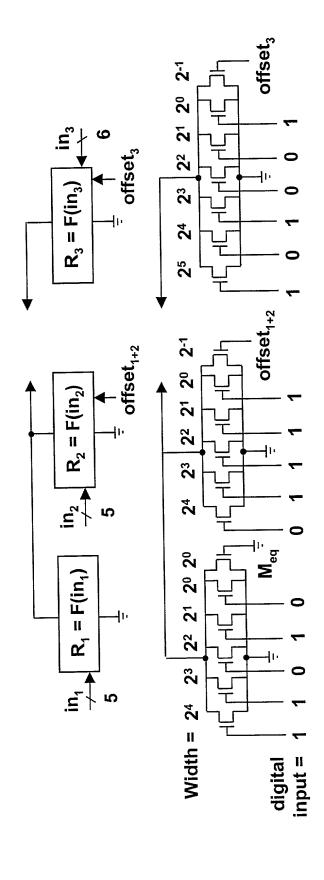


Fig 24A

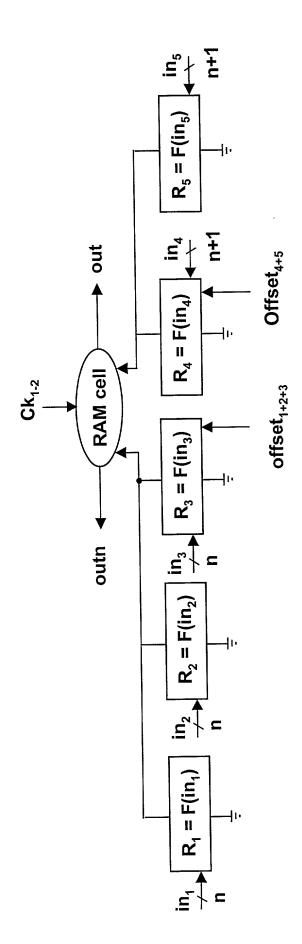
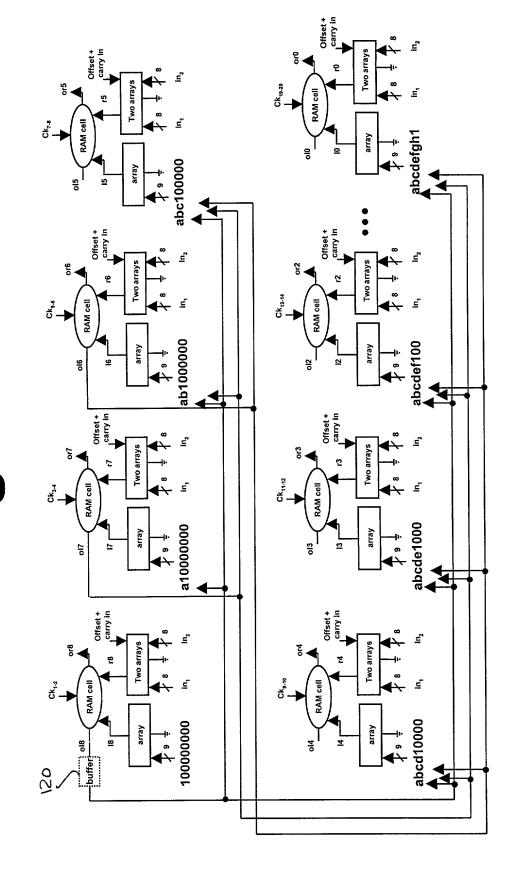
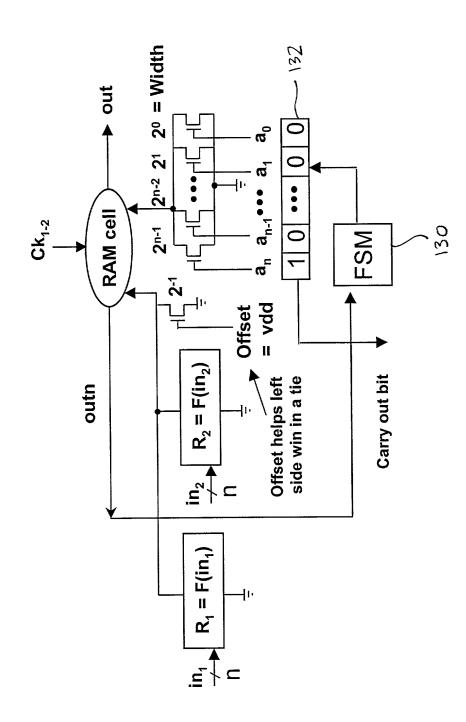
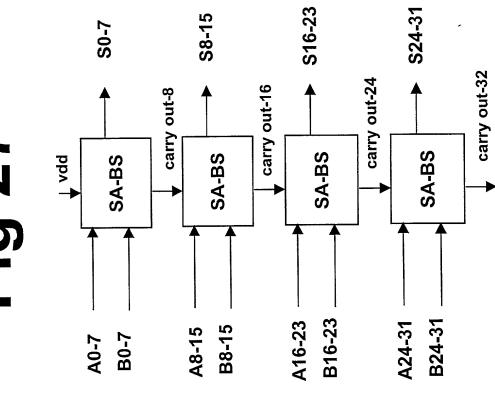


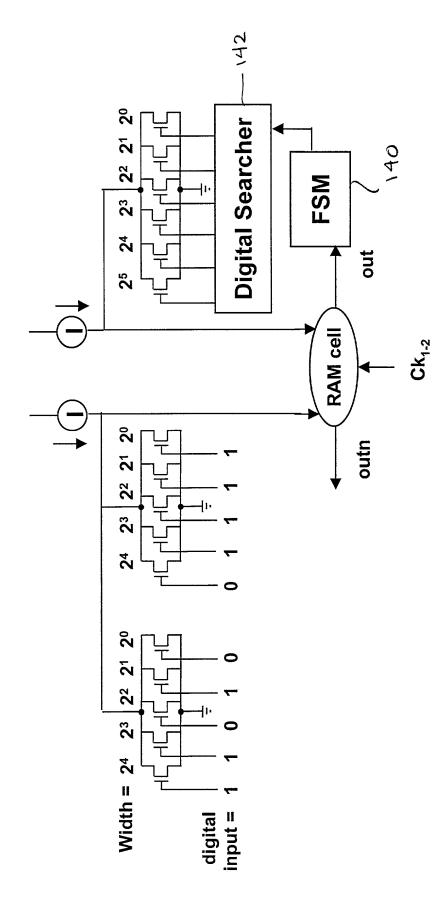
Fig 24B

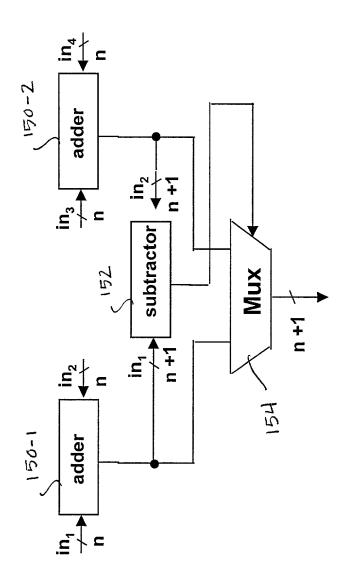




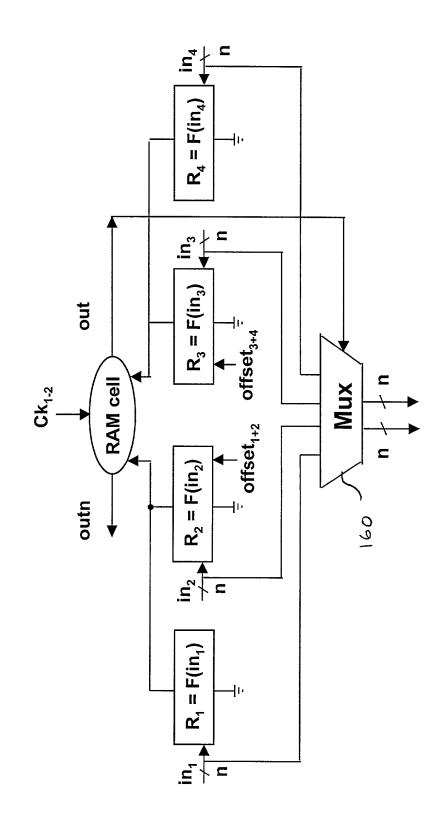
```
/* initialize */
n=8;
a[n] = 1;
for (k=1; k< n; k++)
a[k]=0;
for (i=0; i<n; ++i) /* i = clock tick */
if (out = 0)
a[n-i-1] = 1;
else {
a[n-i-1] = 0;
if (i < n - 1)
a[n-i-1] = 1;
}
/* addition result is in array a[n] */
```

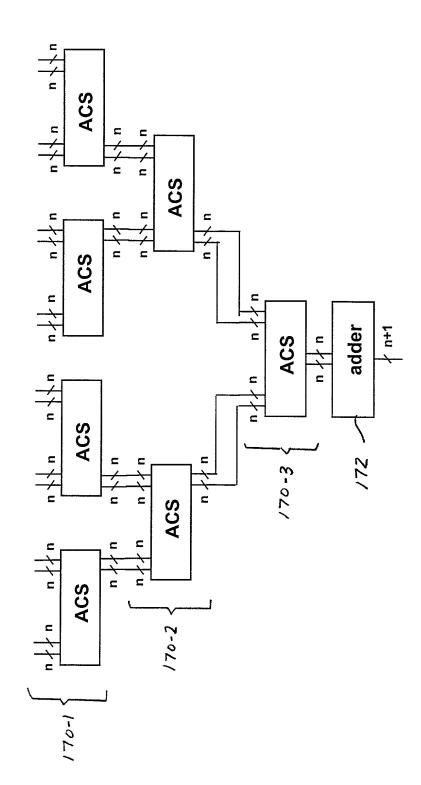






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DELAY (DSEC)	Select	200	21.3 22.4 24.4 24.4 24.4 24.4 24.4 24.4 24		×.
DELAY (DSEC)	V Ado	7120 e	516		
TRANSISTOR COUNT	Select	289	108		X0.54
TRAN	AC AC	5215	55	- 3>- de	oo XA2
POWER (IIW)	dd Select	pare 	300	The state of the s	99'AX
	V	onal 20	M		etor
		Conventional	SeeSaw		Gain Factor

